



## IO-Link Data Map

This document refers to the following IODD file: Banner\_Engineering-R130C-8P22-KQ-20231101-IODD1.1.xml. The IODD file and support files can be found on [www.bannerengineering.com](http://www.bannerengineering.com) under the download section of the product family page.

### Communication Parameters

The following communication parameters are used.

Parameter	Value
IO-Link revision	V1.1
Process Data In length	16-bits
Process Data Out length	16-bits
Bit Rate	38400 bps
Minimum cycle time	3.3 ms
Device ID	659475
Port class	A
SIO mode	Yes
Smart Sensor Profile	No
Block parameterization	Yes
Data Storage	Yes

### IO-Link Process Data In (Device to Master)

#### .Process Data Grouping = 0 (Default)

Subindex	Name	Number of Bits	Data Values
1	Port 1 Discrete1 Input State	1	False = Inactive, True = Active
2	Port 1 Discrete2 Input State	1	False = Inactive, True = Active
3	Port 2 Discrete1 Input State	1	False = Inactive, True = Active
4	Port 2 Discrete2 Input State	1	False = Inactive, True = Active
5	Port 3 Discrete1 Input State	1	False = Inactive, True = Active
6	Port 3 Discrete2 Input State	1	False = Inactive, True = Active
7	Port 4 Discrete1 Input State	1	False = Inactive, True = Active
8	Port 4 Discrete2 Input State	1	False = Inactive, True = Active
9	Port 5 Discrete1 Input State	1	False = Inactive, True = Active
10	Port 5 Discrete2 Input State	1	False = Inactive, True = Active
11	Port 6 Discrete1 Input State	1	False = Inactive, True = Active
12	Port 6 Discrete2 Input State	1	False = Inactive, True = Active
13	Port 7 Discrete1 Input State	1	False = Inactive, True = Active
14	Port 7 Discrete2 Input State	1	False = Inactive, True = Active
15	Port 8 Discrete1 Input State	1	False = Inactive, True = Active
16	Port 8 Discrete2 Input State	1	False = Inactive, True = Active

Octet 0								
Subindex	8	7	6	5	4	3	2	1
Bit offset	15	14	13	12	11	10	9	8
Value	1	1	1	1	1	1	0	1

Octet 1								
Subindex	16	15	14	13	12	11	10	9
Bit offset	7	6	5	4	3	2	1	0
Value	1	1	1	0	1	1	1	0

### Example Based Upon the Value Above:

Subindex	Description	Value
1	Port 1 Discrete1 Input State	Active
2	Port 1 Discrete2 Input State	Inactive
3	Port 2 Discrete1 Input State	Active
4	Port 2 Discrete2 Input State	Active
5	Port 3 Discrete1 Input State	Active
6	Port 3 Discrete2 Input State	Active
7	Port 4 Discrete1 Input State	Active
8	Port 4 Discrete2 Input State	Active
9	Port 5 Discrete1 Input State	Inactive
10	Port 5 Discrete2 Input State	Active
11	Port 6 Discrete1 Input State	Active
12	Port 6 Discrete2 Input State	Active
13	Port 7 Discrete1 Input State	Inactive
14	Port 7 Discrete2 Input State	Active
15	Port 8 Discrete1 Input State	Active
16	Port 8 Discrete2 Input State	Active

### .Process Data Grouping = 1

Subindex	Name	Number of Bits	Data Values
1	Port 1 Discrete1 Input State	1	False = Inactive, True = Active
2	Port 2 Discrete1 Input State	1	False = Inactive, True = Active
3	Port 3 Discrete1 Input State	1	False = Inactive, True = Active
4	Port 4 Discrete1 Input State	1	False = Inactive, True = Active
5	Port 5 Discrete1 Input State	1	False = Inactive, True = Active
6	Port 6 Discrete1 Input State	1	False = Inactive, True = Active
7	Port 7 Discrete1 Input State	1	False = Inactive, True = Active
8	Port 8 Discrete1 Input State	1	False = Inactive, True = Active
9	Port 1 Discrete2 Input State	1	False = Inactive, True = Active
10	Port 2 Discrete2 Input State	1	False = Inactive, True = Active
11	Port 3 Discrete2 Input State	1	False = Inactive, True = Active
12	Port 4 Discrete2 Input State	1	False = Inactive, True = Active
13	Port 5 Discrete2 Input State	1	False = Inactive, True = Active
14	Port 6 Discrete2 Input State	1	False = Inactive, True = Active
15	Port 7 Discrete2 Input State	1	False = Inactive, True = Active
16	Port 8 Discrete2 Input State	1	False = Inactive, True = Active

Octet 0								
Subindex	8	7	6	5	4	3	2	1
Bit offset	15	14	13	12	11	10	9	8

Continued on page 3

Continued from page 2

Octet 0								
Value	1	1	1	1	1	1	0	1

  

Octet 1								
Subindex	16	15	14	13	12	11	10	9
Bit offset	7	6	5	4	3	2	1	0
Value	1	1	1	0	1	1	1	0

### Example Based Upon the Value Above:

Subindex	Description	Value
1	Port 1 Discrete1 Input State	Active
2	Port 2 Discrete1 Input State	Inactive
3	Port 3 Discrete1 Input State	Active
4	Port 4 Discrete1 Input State	Active
5	Port 5 Discrete1 Input State	Active
6	Port 6 Discrete1 Input State	Active
7	Port 7 Discrete1 Input State	Active
8	Port 8 Discrete1 Input State	Active
9	Port 1 Discrete2 Input State	Inactive
10	Port 2 Discrete2 Input State	Active
11	Port 3 Discrete2 Input State	Active
12	Port 4 Discrete2 Input State	Active
13	Port 5 Discrete2 Input State	Inactive
14	Port 6 Discrete2 Input State	Active
15	Port 7 Discrete2 Input State	Active
16	Port 8 Discrete2 Input State	Active

## IO-Link Process Data Out (Master to Device)

### .Process Data Grouping = 0 (Default)

Subindex	Name	Number of Bits	Data Values
1	Port 1 Discrete1 Output State	1	False = Off/Inactive, True = On/Active
2	Port 1 Discrete2 Output State	1	False = Off/Inactive, True = On/Active
3	Port 2 Discrete1 Output State	1	False = Off/Inactive, True = On/Active
4	Port 2 Discrete2 Output State	1	False = Off/Inactive, True = On/Active
5	Port 3 Discrete1 Output State	1	False = Off/Inactive, True = On/Active
6	Port 3 Discrete2 Output State	1	False = Off/Inactive, True = On/Active
7	Port 4 Discrete1 Output State	1	False = Off/Inactive, True = On/Active
8	Port 4 Discrete2 Output State	1	False = Off/Inactive, True = On/Active
1	Port 5 Discrete1 Output State	1	False = Off/Inactive, True = On/Active
2	Port 5 Discrete2 Output State	1	False = Off/Inactive, True = On/Active
3	Port 6 Discrete1 Output State	1	False = Off/Inactive, True = On/Active
4	Port 6 Discrete2 Output State	1	False = Off/Inactive, True = On/Active
5	Port 7 Discrete1 Output State	1	False = Off/Inactive, True = On/Active
6	Port 7 Discrete2 Output State	1	False = Off/Inactive, True = On/Active
7	Port 8 Discrete1 Output State	1	False = Off/Inactive, True = On/Active

Continued on page 4

Continued from page 3

Subindex	Name	Number of Bits	Data Values
8	Port 8 Discrete2 Output State	1	False = Off/Inactive, True = On/Active

Octet 0								
Subindex	8	7	6	5	4	3	2	1
Bit offset	15	14	13	12	11	10	9	8

Octet 1								
Subindex	16	15	14	13	12	11	10	9
Bit offset	7	6	5	4	3	2	1	0

## .Process Data Grouping = 1

Subindex	Name	Number of Bits	Data Values
1	Port 1 Discrete1 Output State	1	False = Off/Inactive, True = On/Active
2	Port 2 Discrete1 Output State	1	False = Off/Inactive, True = On/Active
3	Port 3 Discrete1 Output State	1	False = Off/Inactive, True = On/Active
4	Port 4 Discrete1 Output State	1	False = Off/Inactive, True = On/Active
5	Port 5 Discrete1 Output State	1	False = Off/Inactive, True = On/Active
6	Port 6 Discrete1 Output State	1	False = Off/Inactive, True = On/Active
7	Port 7 Discrete1 Output State	1	False = Off/Inactive, True = On/Active
8	Port 8 Discrete1 Output State	1	False = Off/Inactive, True = On/Active
1	Port 1 Discrete2 Output State	1	False = Off/Inactive, True = On/Active
2	Port 2 Discrete2 Output State	1	False = Off/Inactive, True = On/Active
3	Port 3 Discrete2 Output State	1	False = Off/Inactive, True = On/Active
4	Port 4 Discrete2 Output State	1	False = Off/Inactive, True = On/Active
5	Port 5 Discrete2 Output State	1	False = Off/Inactive, True = On/Active
6	Port 6 Discrete2 Output State	1	False = Off/Inactive, True = On/Active
7	Port 7 Discrete2 Output State	1	False = Off/Inactive, True = On/Active
8	Port 8 Discrete2 Output State	1	False = Off/Inactive, True = On/Active

Octet 0								
Subindex	8	7	6	5	4	3	2	1
Bit offset	15	14	13	12	11	10	9	8

Octet 1								
Subindex	16	15	14	13	12	11	10	9
Bit offset	7	6	5	4	3	2	1	0

## Parameters Set Using IO-Link

These parameters can be read from and/or written to an R130C 8-port 2-Channel PNP IO-Link Hub. Also included is information about whether the variable in question is saved during Data Storage and whether the variable came from the IO-Link Smart Sensor Profile.

Unlike Process Data In, which is transmitted from the IO-Link device to the IO-Link master cyclically, these parameters are read or written acyclically as needed.

Index	Subindex	Name	Length	Value Range	Default	Access Rights	Data Storage?
0	1-16	Direct Parameter Page 1 (incl. Vendor ID & Device ID)				RO	
1	1-16	Direct Parameters Page 2				RW	

Continued on page 5

Continued from page 4

Index	Subindex	Name	Length	Value Range	Default	Access Rights	Data Storage?
2		System Command		130 = Restore Factory Settings 162 = Start discovery 163 = Stop discovery 164 = Reset All Metrics		WO	
3		Data Storage Index (device-specific list of parameters to be stored)				RW	
4-11		<i>reserved by IO-Link Specification</i>					
<b>12</b>		<b>Device Access Locks</b>					
12	1	Parameter Write Access Lock		0 = Off, 1 = On	0	RW	Y
12	2	Data Storage Lock		0 = Off, 1 = On	0	RW	Y
12	3	Local Parameterization Lock		0 = Off, 1 = On	0	RW	Y
12	4	Local User Interface Lock		0 = Off, 1 = On	0	RW	Y
16		Vendor Name string		Banner Engineering Corporation		RO	
17		Vendor Text string		More Sensors. More Solutions.		RO	
18		Product Name string		R130C		RO	
19		Product ID string		R130C-8P22-KQ		RO	
20		Product Text string				RO	
21		Serial Number				RO	
23		Firmware Version				RO	
24		App Specific Tag (user defined)				RW	Y
36		Device Status	8-bit integer	0 = Device is OK 1 = Maintenance required 2 = Out of specification 3 = Functional check 4 = Failure 5..255 Reserved		RO	
37		Detailed Device Status	Array[6] of 3-octet			RO	
38-39		<i>reserved</i>					
40		Process Data Input		<i>see Process Data In</i>		RO	
41		Process Data Output		<i>see Process Data Out</i>		RO	
42-57		<i>unused/reserved</i>					
<b>69</b>		<b>All-Time Run Time</b>					
69	1	Run counter	32-bit Unsigned	0..2147483647		RO	Y
<b>70</b>		<b>Resettable Run Time</b>					
70	1	Run counter	32-bit Unsigned	0..2147483647	0	RW	
<b>76</b>		<b>Vendor Specific Configuration</b>					
76	1	Process Data Grouping	8-bit Unsigned	0 = Group by Port, 1 = Group by Channel	0	RW	Y
<b>78</b>		<b>All-Time Run Time Event Time</b>					
78	1	Event Time	32-bit Unsigned	0..2147483647	0	RW	Y
<b>79</b>		<b>Resettable Run Time Event Time</b>					
79	1	Event Time	32-bit Unsigned	0..2147483647	0	RW	Y
<b>80</b>		<b>IO Metrics Ports 1 to 4</b>					
80	1	Port 1 Discrete1 Count	32-bit Unsigned	0..2147483647		RO	

Continued on page 6

Continued from page 5

Index	Subindex	Name	Length	Value Range	Default	Access Rights	Data Storage?
80	2	Port 1 Discrete1 Duration	32-bit Unsigned	0..2147483647, 50 µS resolution		RO	
80	3	Port 1 Discrete1 Events per Minute	32-bit Unsigned	1..37500		RO	
80	4	Port 1 Discrete1 Totalizer Counter	32-bit Unsigned	0..2147483647		RO	
80	5	Port 1 Discrete2 Count	32-bit Unsigned	0..2147483647		RO	
80	6	Port 1 Discrete2 Duration	32-bit Unsigned	0..2147483647, 50 µS resolution		RO	
80	7	Port 1 Discrete2 Events per Minute	32-bit Unsigned	1..37500		RO	
80	8	Port 1 Discrete2 Totalizer Counter	32-bit Unsigned	0..2147483647		RO	
80	9	Port 2 Discrete1 Count	32-bit Unsigned	0..2147483647		RO	
80	10	Port 2 Discrete1 Duration	32-bit Unsigned	0..2147483647, 50 µS resolution		RO	
80	11	Port 2 Discrete1 Events per Minute	32-bit Unsigned	1..37500		RO	
80	12	Port 2 Discrete1 Totalizer Counter	32-bit Unsigned	0..2147483647		RO	
80	13	Port 2 Discrete2 Count	32-bit Unsigned	0..2147483647		RO	
80	14	Port 2 Discrete2 Duration	32-bit Unsigned	0..2147483647, 50 µS resolution		RO	
80	15	Port 2 Discrete2 Events per Minute	32-bit Unsigned	1..37500		RO	
80	16	Port 2 Discrete2 Totalizer Counter	32-bit Unsigned	0..2147483647		RO	
80	17	Port 3 Discrete1 Count	32-bit Unsigned	0..2147483647		RO	
80	18	Port 3 Discrete1 Duration	32-bit Unsigned	0..2147483647, 50 µS resolution		RO	
80	19	Port 3 Discrete1 Events per Minute	32-bit Unsigned	1..37500		RO	
80	20	Port 3 Discrete1 Totalizer Counter	32-bit Unsigned	0..2147483647		RO	
80	21	Port 3 Discrete2 Count	32-bit Unsigned	0..2147483647		RO	
80	22	Port 3 Discrete2 Duration	32-bit Unsigned	0..2147483647, 50 µS resolution		RO	
80	23	Port 3 Discrete2 Events per Minute	32-bit Unsigned	1..37500		RO	
80	24	Port 3 Discrete2 Totalizer Counter	32-bit Unsigned	0..2147483647		RO	
80	25	Port 4 Discrete1 Count	32-bit Unsigned	0..2147483647		RO	
80	26	Port 4 Discrete1 Duration	32-bit Unsigned	0..2147483647, 50 µS resolution		RO	
80	27	Port 4 Discrete1 Events per Minute	32-bit Unsigned	1..37500		RO	
80	28	Port 4 Discrete1 Totalizer Counter	32-bit Unsigned	0..2147483647		RO	
80	29	Port 4 Discrete2 Count	32-bit Unsigned	0..2147483647		RO	
80	30	Port 4 Discrete2 Duration	32-bit Unsigned	0..2147483647, 50 µS resolution		RO	

Continued on page 7

Continued from page 6

Index	Subindex	Name	Length	Value Range	Default	Access Rights	Data Storage?
80	31	Port 4 Discrete2 Events per Minute	32-bit UInteger	1..37500		RO	
80	32	Port 4 Discrete2 Totalizer Counter	32-bit UInteger	0..2147483647		RO	
<b>81</b>		<b>IO Metrics Ports 5 to 8</b>					
81	1	Port 5 Discrete1 Count	32-bit UInteger	0..2147483647		RO	
81	2	Port 5 Discrete1 Duration -	32-bit UInteger	0..2147483647, 50 $\mu$ S resolution		RO	
81	3	Port 5 Discrete1 Events per Minute	32-bit UInteger	1..37500		RO	
81	4	Port 5 Discrete1 Totalizer Counter	32-bit UInteger	0..2147483647		RO	
81	5	Port 5 Discrete2 Count	32-bit UInteger	0..2147483647		RO	
81	6	Port 5 Discrete2 Duration	32-bit UInteger	0..2147483647, 50 $\mu$ S resolution		RO	
81	7	Port 5 Discrete2 Events per Minute	32-bit UInteger	1..37500		RO	
81	8	Port 5 Discrete2 Totalizer Counter	32-bit UInteger	0..2147483647		RO	
81	9	Port 6 Discrete1 Count	32-bit UInteger	0..2147483647		RO	
81	10	Port 6 Discrete1 Duration	32-bit UInteger	0..2147483647, 50 $\mu$ S resolution		RO	
81	11	Port 6 Discrete1 Events per Minute	32-bit UInteger	1..37500		RO	
81	12	Port 6 Discrete1 Totalizer Counter	32-bit UInteger	0..2147483647		RO	
81	13	Port 6 Discrete2 Count	32-bit UInteger	0..2147483647		RO	
81	14	Port 6 Discrete2 Duration	32-bit UInteger	0..2147483647, 50 $\mu$ S resolution		RO	
81	15	Port 6 Discrete2 Events per Minute	32-bit UInteger	1..37500		RO	
81	16	Port 6 Discrete2 Totalizer Counter	32-bit UInteger	0..2147483647		RO	
81	17	Port 7 Discrete1 Count	32-bit UInteger	0..2147483647		RO	
81	18	Port 7 Discrete1 Duration	32-bit UInteger	0..2147483647, 50 $\mu$ S resolution		RO	
81	19	Port 7 Discrete1 Events per Minute	32-bit UInteger	1..37500		RO	
81	20	Port 7 Discrete1 Totalizer Counter	32-bit UInteger	0..2147483647		RO	
81	21	Port 7 Discrete2 Count	32-bit UInteger	0..2147483647		RO	
81	22	Port 7 Discrete2 Duration	32-bit UInteger	0..2147483647, 50 $\mu$ S resolution		RO	
81	23	Port 7 Discrete2 Events per Minute	32-bit UInteger	1..37500		RO	
81	24	Port 7 Discrete2 Totalizer Counter	32-bit UInteger	0..2147483647		RO	
81	25	Port 8 Discrete1 Count	32-bit UInteger	0..2147483647		RO	
81	26	Port 8 Discrete1 Duration	32-bit UInteger	0..2147483647, 50 $\mu$ S resolution		RO	
81	27	Port 8 Discrete1 Events per Minute	32-bit UInteger	1..37500		RO	

Continued on page 8

Continued from page 7

Index	Subindex	Name	Length	Value Range	Default	Access Rights	Data Storage?
81	28	Port 8 Discrete1 Totalizer Counter	32-bit Unsigned	0..2147483647		RO	
81	29	Port 8 Discrete2 Count	32-bit Unsigned	0..2147483647		RO	
81	30	Port 8 Discrete2 Duration	32-bit Unsigned	0..2147483647, 50 µS resolution		RO	
81	31	Port 8 Discrete2 Events per Minute	32-bit Unsigned	1..37500		RO	
81	32	Port 8 Discrete2 Totalizer Counter	32-bit Unsigned	0..2147483647		RO	
<b>82</b>		<b>Selectable Metric Reset</b>					
82	1	Port 1 Discrete1	Boolean	False = Do Not Reset, True = Reset	False	RW	
82	2	Port 1 Discrete2	Boolean	False = Do Not Reset, True = Reset	False	RW	
82	3	Port 2 Discrete1	Boolean	False = Do Not Reset, True = Reset	False	RW	
82	4	Port 2 Discrete2	Boolean	False = Do Not Reset, True = Reset	False	RW	
82	5	Port 3 Discrete1	Boolean	False = Do Not Reset, True = Reset	False	RW	
82	6	Port 3 Discrete2	Boolean	False = Do Not Reset, True = Reset	False	RW	
82	7	Port 4 Discrete1	Boolean	False = Do Not Reset, True = Reset	False	RW	
82	8	Port 4 Discrete2	Boolean	False = Do Not Reset, True = Reset	False	RW	
82	9	Port 5 Discrete1	Boolean	False = Do Not Reset, True = Reset	False	RW	
82	10	Port 5 Discrete2	Boolean	False = Do Not Reset, True = Reset	False	RW	
82	11	Port 6 Discrete1	Boolean	False = Do Not Reset, True = Reset	False	RW	
82	12	Port 6 Discrete2	Boolean	False = Do Not Reset, True = Reset	False	RW	
82	13	Port 7 Discrete1	Boolean	False = Do Not Reset, True = Reset	False	RW	
82	14	Port 7 Discrete2	Boolean	False = Do Not Reset, True = Reset	False	RW	
82	15	Port 8 Discrete1	Boolean	False = Do Not Reset, True = Reset	False	RW	
82	16	Port 8 Discrete2	Boolean	False = Do Not Reset, True = Reset	False	RW	
82	17	Port 1 Discrete1 Reset Count	32-bit Unsigned	0..2147483647	0	RW	
82	18	Port 1 Discrete1 Reset Count	32-bit Unsigned	0..2147483647	0	RW	
82	19	Port 2 Discrete1 Reset Count	32-bit Unsigned	0..2147483647	0	RW	
82	20	Port 2 Discrete1 Reset Count	32-bit Unsigned	0..2147483647	0	RW	
82	21	Port 3 Discrete1 Reset Count	32-bit Unsigned	0..2147483647	0	RW	
82	22	Port 3 Discrete1 Reset Count	32-bit Unsigned	0..2147483647	0	RW	
82	23	Port 4 Discrete1 Reset Count	32-bit Unsigned	0..2147483647	0	RW	
82	24	Port 4 Discrete1 Reset Count	32-bit Unsigned	0..2147483647	0	RW	
82	25	Port 5 Discrete1 Reset Count	32-bit Unsigned	0..2147483647	0	RW	
82	26	Port 5 Discrete1 Reset Count	32-bit Unsigned	0..2147483647	0	RW	
82	27	Port 6 Discrete1 Reset Count	32-bit Unsigned	0..2147483647	0	RW	
82	28	Port 6 Discrete1 Reset Count	32-bit Unsigned	0..2147483647	0	RW	
82	29	Port 7 Discrete1 Reset Count	32-bit Unsigned	0..2147483647	0	RW	

Continued on page 9



Continued from page 8

Index	Subindex	Name	Length	Value Range	Default	Access Rights	Data Storage?
82	30	Port 7 Discrete1 Reset Count	32-bit Uinteger	0..2147483647	0	RW	
82	31	Port 8 Discrete1 Reset Count	32-bit Uinteger	0..2147483647	0	RW	
82	32	Port 8 Discrete1 Reset Count	32-bit Uinteger	0..2147483647	0	RW	
<b>87</b>		<b>Port 1 Configuration</b>					
87	1	Discrete1 IO Selection	8-bit Uinteger	1 = PNP Input, 3 = PNP Output with Pull Down	0	RW	Y
87	2	Discrete1 Delay Mode	8-bit Uinteger	0 = Disabled 1 = On Off Delay 2 = On One-shot 3 = Off One-shot 4 = On Pulse-stretcher 5 = Off Pulse-stretcher 6 = Totalizer 7 = Retriquerable On One-shot 8 = Retriquerable Off One-Shot	0	RW	Y
87	3	Discrete1 Delay Timer 1	32-bit Uinteger	0..2147483647 [Discrete1 On Delay, One-shot, Pulse-stretcher time(ms) or Totalizer Count]	0	RW	Y
87	4	Discrete1 Delay Timer 2	32-bit Uinteger	0..2147483647 (Discrete1 Off Delay or Totalizer time)ms	0	RW	Y
87	5	Discrete1 Mirroring Enable	8-bit Uinteger	0 = Disabled, 1 = Enabled	0	RW	Y
87	6	Discrete1 Mirroring Port Selection	8-bit Uinteger	0 = Port 1 1 = Port 2 2 = Port 3 3 = Port 4 4 = Port 5 5 = Port 6 6 = Port 7 7 = Port 8	0	RW	Y
87	7	Discrete1 Mirroring Channel Selection	8-bit Uinteger	0 = Discrete1, 1 = Discrete2	0	RW	Y
87	8	Discrete1 Mirroring Inversion	8-bit Uinteger	0 = Not Inverted, 1 = Inverted	0	RW	Y
87	9	Discrete2 IO Selection	8-bit Uinteger	1 = PNP Input, 3 = PNP Output with Pull Down	0	RW	Y
87	10	Discrete2 Delay Mode	8-bit Uinteger	0 = Disabled 1 = On Off Delay 2 = On One-shot 3 = Off One-shot 4 = On Pulse-stretcher 5 = Off Pulse-stretcher 6 = Totalizer 7 = Retriquerable On One-shot 8 = Retriquerable Off One-Shot	0	RW	Y
87	11	Discrete2 Delay Timer 1	32-bit Uinteger	0..2147483647 [Discrete2 On Delay, One-shot, Pulse-stretcher time(ms) or Totalizer Count]	0	RW	Y
87	12	Discrete2 Delay Timer 2	32-bit Uinteger	0..2147483647 (Discrete2 Off Delay or Totalizer time)ms	0	RW	Y
87	13	Discrete2 Mirroring Enable	8-bit Uinteger	0 = Disabled, 1 = Enabled	0	RW	Y

Continued on page 10

Continued from page 9

Index	Subindex	Name	Length	Value Range	Default	Access Rights	Data Storage?
87	14	Discrete2 Mirroring Port Selection	8-bit Uinteger	0 = Port 1 1 = Port 2 2 = Port 3 3 = Port 4 4 = Port 5 5 = Port 6 6 = Port 7 7 = Port 8	0	RW	Y
87	15	Discrete2 Mirroring Channel Selection	8-bit Uinteger	0 = Discrete1, 1 = Discrete2	0	RW	Y
87	16	Discrete2 Mirroring Inversion	8-bit Uinteger	0 = Not Inverted, 1 = Inverted	0	RW	Y
<b>88</b>		<b>Port 2 Configuration</b>					
88	1	Discrete1 IO Selection	8-bit Uinteger	1 = PNP Input, 3 = PNP Output with Pull Down	0	RW	Y
88	2	Discrete1 Delay Mode	8-bit Uinteger	0 = Disabled 1 = On Off Delay 2 = On One-shot 3 = Off One-shot 4 = On Pulse-stretcher 5 = Off Pulse-stretcher 6 = Totalizer 7 = Retriggerable On One-shot 8 = Retriggerable Off One-Shot	0	RW	Y
88	3	Discrete1 Delay Timer 1	32-bit Uinteger	0..2147483647 [Discrete1 On Delay, One-shot, Pulse-stretcher time(ms) or Totalizer Count]	0	RW	Y
88	4	Discrete1 Delay Timer 2	32-bit Uinteger	0..2147483647 (Discrete1 Off Delay or Totalizer time)ms	0	RW	Y
88	5	Discrete1 Mirroring Enable	8-bit Uinteger	0 = Disabled, 1 = Enabled	0	RW	Y
88	6	Discrete1 Mirroring Port Selection	8-bit Uinteger	0 = Port 1 1 = Port 2 2 = Port 3 3 = Port 4 4 = Port 5 5 = Port 6 6 = Port 7 7 = Port 8	0	RW	Y
88	7	Discrete1 Mirroring Channel Selection	8-bit Uinteger	0 = Discrete1, 1 = Discrete2	0	RW	Y
88	8	Discrete1 Mirroring Inversion	8-bit Uinteger	0 = Not Inverted, 1 = Inverted	0	RW	Y
88	9	Discrete2 IO Selection	8-bit Uinteger	1 = PNP Input, 3 = PNP Output with Pull Down	0	RW	Y
88	10	Discrete2 Delay Mode	8-bit Uinteger	0 = Disabled 1 = On Off Delay 2 = On One-shot 3 = Off One-shot 4 = On Pulse-stretcher 5 = Off Pulse-stretcher 6 = Totalizer 7 = Retriggerable On One-shot 8 = Retriggerable Off One-Shot	0	RW	Y

Continued on page 11

Continued from page 10

Index	Subindex	Name	Length	Value Range	Default	Access Rights	Data Storage?
88	11	Discrete2 Delay Timer 1	32-bit Uinteger	0..2147483647 [Discrete2 On Delay, One-shot, Pulse-stretcher time(ms) or Totalizer Count]	0	RW	Y
88	12	Discrete2 Delay Timer 2	32-bit Uinteger	0..2147483647 (Discrete2 Off Delay or Totalizer time)ms	0	RW	Y
88	13	Discrete2 Mirroring Enable	8-bit Uinteger	0 = Disabled, 1 = Enabled	0	RW	Y
88	14	Discrete2 Mirroring Port Selection	8-bit Uinteger	0 = Port 1 1 = Port 2 2 = Port 3 3 = Port 4 4 = Port 5 5 = Port 6 6 = Port 7 7 = Port 8	0	RW	Y
88	15	Discrete2 Mirroring Channel Selection	8-bit Uinteger	0 = Discrete1, 1 = Discrete2	0	RW	Y
88	16	Discrete2 Mirroring Inversion	8-bit Uinteger	0 = Not Inverted, 1 = Inverted	0	RW	Y
<b>89</b>		<b>Port 3 Configuration</b>					
89	1	Discrete1 IO Selection	8-bit Uinteger	1 = PNP Input, 3 = PNP Output with Pull Down	0	RW	Y
89	2	Discrete1 Delay Mode	8-bit Uinteger	0 = Disabled 1 = On Off Delay 2 = On One-shot 3 = Off One-shot 4 = On Pulse-stretcher 5 = Off Pulse-stretcher 6 = Totalizer 7 = Retriggerable On One-shot 8 = Retriggerable Off One-Shot	0	RW	Y
89	3	Discrete1 Delay Timer 1	32-bit Uinteger	0..2147483647 [Discrete1 On Delay, One-shot, Pulse-stretcher time(ms) or Totalizer Count]	0	RW	Y
89	4	Discrete1 Delay Timer 2	32-bit Uinteger	0..2147483647 (Discrete1 Off Delay or Totalizer time)ms	0	RW	Y
89	5	Discrete1 Mirroring Enable	8-bit Uinteger	0 = Disabled, 1 = Enabled	0	RW	Y
89	6	Discrete1 Mirroring Port Selection	8-bit Uinteger	0 = Port 1 1 = Port 2 2 = Port 3 3 = Port 4 4 = Port 5 5 = Port 6 6 = Port 7 7 = Port 8	0	RW	Y
89	7	Discrete1 Mirroring Channel Selection	8-bit Uinteger	0 = Discrete1, 1 = Discrete2	0	RW	Y
89	8	Discrete1 Mirroring Inversion	8-bit Uinteger	0 = Not Inverted, 1 = Inverted	0	RW	Y
89	9	Discrete2 IO Selection	8-bit Uinteger	1 = PNP Input, 3 = PNP Output with Pull Down	0	RW	Y

Continued on page 12

Continued from page 11

Index	Subindex	Name	Length	Value Range	Default	Access Rights	Data Storage?
89	10	Discrete2 Delay Mode	8-bit Uinteger	0 = Disabled 1 = On Off Delay 2 = On One-shot 3 = Off One-shot 4 = On Pulse-stretcher 5 = Off Pulse-stretcher 6 = Totalizer 7 = Retriggerable On One-shot 8 = Retriggerable Off One-Shot	0	RW	Y
89	11	Discrete2 Delay Timer 1	32-bit Uinteger	0..2147483647 [Discrete2 On Delay, One-shot, Pulse-stretcher time(ms) or Totalizer Count]	0	RW	Y
89	12	Discrete2 Delay Timer 2	32-bit Uinteger	0..2147483647 (Discrete2 Off Delay or Totalizer time)ms	0	RW	Y
89	13	Discrete2 Mirroring Enable	8-bit Uinteger	0 = Disabled, 1 = Enabled	0	RW	Y
89	14	Discrete2 Mirroring Port Selection	8-bit Uinteger	0 = Port 1 1 = Port 2 2 = Port 3 3 = Port 4 4 = Port 5 5 = Port 6 6 = Port 7 7 = Port 8	0	RW	Y
89	15	Discrete2 Mirroring Channel Selection	8-bit Uinteger	0 = Discrete1, 1 = Discrete2	0	RW	Y
89	16	Discrete2 Mirroring Inversion	8-bit Uinteger	0 = Not Inverted, 1 = Inverted	0	RW	Y
<b>90</b>		<b>Port 4 Configuration</b>					
90	1	Discrete1 IO Selection	8-bit Uinteger	1 = PNP Input, 3 = PNP Output with Pull Down	0	RW	Y
90	2	Discrete1 Delay Mode	8-bit Uinteger	0 = Disabled 1 = On Off Delay 2 = On One-shot 3 = Off One-shot 4 = On Pulse-stretcher 5 = Off Pulse-stretcher 6 = Totalizer 7 = Retriggerable On One-shot 8 = Retriggerable Off One-Shot	0	RW	Y
90	3	Discrete1 Delay Timer 1	32-bit Uinteger	0..2147483647 [Discrete1 On Delay, One-shot, Pulse-stretcher time(ms) or Totalizer Count]	0	RW	Y
90	4	Discrete1 Delay Timer 2	32-bit Uinteger	0..2147483647 (Discrete1 Off Delay or Totalizer time)ms	0	RW	Y
90	5	Discrete1 Mirroring Enable	8-bit Uinteger	0 = Disabled, 1 = Enabled	0	RW	Y

Continued on page 13

Continued from page 12

Index	Subindex	Name	Length	Value Range	Default	Access Rights	Data Storage?
90	6	Discrete1 Mirroring Port Selection	8-bit Uinteger	0 = Port 1 1 = Port 2 2 = Port 3 3 = Port 4 4 = Port 5 5 = Port 6 6 = Port 7 7 = Port 8	0	RW	Y
90	7	Discrete1 Mirroring Channel Selection	8-bit Uinteger	0 = Discrete1, 1 = Discrete2	0	RW	Y
90	8	Discrete1 Mirroring Inversion	8-bit Uinteger	0 = Not Inverted, 1 = Inverted	0	RW	Y
90	9	Discrete2 IO Selection	8-bit Uinteger	1 = PNP Input, 3 = PNP Output with Pull Down	0	RW	Y
90	10	Discrete2 Delay Mode	8-bit Uinteger	0 = Disabled 1 = On Off Delay 2 = On One-shot 3 = Off One-shot 4 = On Pulse-stretcher 5 = Off Pulse-stretcher 6 = Totalizer 7 = Retriggerable On One-shot 8 = Retriggerable Off One-Shot	0	RW	Y
90	11	Discrete2 Delay Timer 1	32-bit Uinteger	0..2147483647 [Discrete2 On Delay, One-shot, Pulse-stretcher time(ms) or Totalizer Count]	0	RW	Y
90	12	Discrete2 Delay Timer 2	32-bit Uinteger	0..2147483647 (Discrete2 Off Delay or Totalizer time)ms	0	RW	Y
90	13	Discrete2 Mirroring Enable	8-bit Uinteger	0 = Disabled, 1 = Enabled	0	RW	Y
90	14	Discrete2 Mirroring Port Selection	8-bit Uinteger	0 = Port 1 1 = Port 2 2 = Port 3 3 = Port 4 4 = Port 5 5 = Port 6 6 = Port 7 7 = Port 8	0	RW	Y
90	15	Discrete2 Mirroring Channel Selection	8-bit Uinteger	0 = Discrete1, 1 = Discrete2	0	RW	Y
90	16	Discrete2 Mirroring Inversion	8-bit Uinteger	0 = Not Inverted, 1 = Inverted	0	RW	Y
<b>91</b>		<b>Port 5 Configuration</b>					
91	1	Discrete1 IO Selection	8-bit Uinteger	1 = PNP Input, 3 = PNP Output with Pull Down	0	RW	Y
91	2	Discrete1 Delay Mode	8-bit Uinteger	0 = Disabled 1 = On Off Delay 2 = On One-shot 3 = Off One-shot 4 = On Pulse-stretcher 5 = Off Pulse-stretcher 6 = Totalizer 7 = Retriggerable On One-shot 8 = Retriggerable Off One-Shot	0	RW	Y

Continued on page 14

Continued from page 13

Index	Subindex	Name	Length	Value Range	Default	Access Rights	Data Storage?
91	3	Discrete1 Delay Timer 1	32-bit Uinteger	0..2147483647 [Discrete1 On Delay, One-shot, Pulse-stretcher time(ms) or Totalizer Count]	0	RW	Y
91	4	Discrete1 Delay Timer 2	32-bit Uinteger	0..2147483647 (Discrete1 Off Delay or Totalizer time)ms	0	RW	Y
91	5	Discrete1 Mirroring Enable	8-bit Uinteger	0 = Disabled, 1 = Enabled	0	RW	Y
91	6	Discrete1 Mirroring Port Selection	8-bit Uinteger	0 = Port 1 1 = Port 2 2 = Port 3 3 = Port 4 4 = Port 5 5 = Port 6 6 = Port 7 7 = Port 8	0	RW	Y
91	7	Discrete1 Mirroring Channel Selection	8-bit Uinteger	0 = Discrete1, 1 = Discrete2	0	RW	Y
91	8	Discrete1 Mirroring Inversion	8-bit Uinteger	0 = Not Inverted, 1 = Inverted	0	RW	Y
91	9	Discrete2 IO Selection	8-bit Uinteger	1 = PNP Input, 3 = PNP Output with Pull Down	0	RW	Y
91	10	Discrete2 Delay Mode	8-bit Uinteger	0 = Disabled 1 = On Off Delay 2 = On One-shot 3 = Off One-shot 4 = On Pulse-stretcher 5 = Off Pulse-stretcher 6 = Totalizer 7 = Retriggerable On One-shot 8 = Retriggerable Off One-Shot	0	RW	Y
91	11	Discrete2 Delay Timer 1	32-bit Uinteger	0..2147483647 [Discrete2 On Delay, One-shot, Pulse-stretcher time(ms) or Totalizer Count]	0	RW	Y
91	12	Discrete2 Delay Timer 2	32-bit Uinteger	0..2147483647 (Discrete2 Off Delay or Totalizer time)ms	0	RW	Y
91	13	Discrete2 Mirroring Enable	8-bit Uinteger	0 = Disabled, 1 = Enabled	0	RW	Y
91	14	Discrete2 Mirroring Port Selection	8-bit Uinteger	0 = Port 1 1 = Port 2 2 = Port 3 3 = Port 4 4 = Port 5 5 = Port 6 6 = Port 7 7 = Port 8	0	RW	Y
91	15	Discrete2 Mirroring Channel Selection	8-bit Uinteger	0 = Discrete1, 1 = Discrete2	0	RW	Y
91	16	Discrete2 Mirroring Inversion	8-bit Uinteger	0 = Not Inverted, 1 = Inverted	0	RW	Y
<b>92</b>		<b>Port 6 Configuration</b>					
92	1	Discrete1 IO Selection	8-bit Uinteger	1 = PNP Input, 3 = PNP Output with Pull Down	0	RW	Y

Continued on page 15

Continued from page 14

Index	Subindex	Name	Length	Value Range	Default	Access Rights	Data Storage?
92	2	Discrete1 Delay Mode	8-bit Uinteger	0 = Disabled 1 = On Off Delay 2 = On One-shot 3 = Off One-shot 4 = On Pulse-stretcher 5 = Off Pulse-stretcher 6 = Totalizer 7 = Retriggerable On One-shot 8 = Retriggerable Off One-Shot	0	RW	Y
92	3	Discrete1 Delay Timer 1	32-bit Uinteger	0..2147483647 [Discrete1 On Delay, One-shot, Pulse-stretcher time(ms) or Totalizer Count]	0	RW	Y
92	4	Discrete1 Delay Timer 2	32-bit Uinteger	0..2147483647 (Discrete1 Off Delay or Totalizer time)ms	0	RW	Y
92	5	Discrete1 Mirroring Enable	8-bit Uinteger	0 = Disabled, 1 = Enabled	0	RW	Y
92	6	Discrete1 Mirroring Port Selection	8-bit Uinteger	0 = Port 1 1 = Port 2 2 = Port 3 3 = Port 4 4 = Port 5 5 = Port 6 6 = Port 7 7 = Port 8	0	RW	Y
92	7	Discrete1 Mirroring Channel Selection	8-bit Uinteger	0 = Discrete1, 1 = Discrete2	0	RW	Y
92	8	Discrete1 Mirroring Inversion	8-bit Uinteger	0 = Not Inverted, 1 = Inverted	0	RW	Y
92	9	Discrete2 IO Selection	8-bit Uinteger	1 = PNP Input, 3 = PNP Output with Pull Down	0	RW	Y
92	10	Discrete2 Delay Mode	8-bit Uinteger	0 = Disabled 1 = On Off Delay 2 = On One-shot 3 = Off One-shot 4 = On Pulse-stretcher 5 = Off Pulse-stretcher 6 = Totalizer 7 = Retriggerable On One-shot 8 = Retriggerable Off One-Shot	0	RW	Y
92	11	Discrete2 Delay Timer 1	32-bit Uinteger	0..2147483647 [Discrete2 On Delay, One-shot, Pulse-stretcher time(ms) or Totalizer Count]	0	RW	Y
92	12	Discrete2 Delay Timer 2	32-bit Uinteger	0..2147483647 (Discrete2 Off Delay or Totalizer time)ms	0	RW	Y
92	13	Discrete2 Mirroring Enable	8-bit Uinteger	0 = Disabled, 1 = Enabled	0	RW	Y
92	14	Discrete2 Mirroring Port Selection	8-bit Uinteger	0 = Port 1 1 = Port 2 2 = Port 3 3 = Port 4 4 = Port 5 5 = Port 6 6 = Port 7 7 = Port 8	0	RW	Y

Continued on page 16

Continued from page 15

Index	Subindex	Name	Length	Value Range	Default	Access Rights	Data Storage?
92	15	Discrete2 Mirroring Channel Selection	8-bit Uinteger	0 = Discrete1, 1 = Discrete2	0	RW	Y
92	16	Discrete2 Mirroring Inversion	8-bit Uinteger	0 = Not Inverted, 1 = Inverted	0	RW	Y
<b>93</b>		<b>Port 7 Configuration</b>					
93	1	Discrete1 IO Selection	8-bit Uinteger	1 = PNP Input, 3 = PNP Output with Pull Down	0	RW	Y
93	2	Discrete1 Delay Mode	8-bit Uinteger	0 = Disabled 1 = On Off Delay 2 = On One-shot 3 = Off One-shot 4 = On Pulse-stretcher 5 = Off Pulse-stretcher 6 = Totalizer 7 = Retriggerable On One-shot 8 = Retriggerable Off One-Shot	0	RW	Y
93	3	Discrete1 Delay Timer 1	32-bit Uinteger	0..2147483647 [Discrete1 On Delay, One-shot, Pulse-stretcher time(ms) or Totalizer Count]	0	RW	Y
93	4	Discrete1 Delay Timer 2	32-bit Uinteger	0..2147483647 (Discrete1 Off Delay or Totalizer time)ms	0	RW	Y
93	5	Discrete1 Mirroring Enable	8-bit Uinteger	0 = Disabled, 1 = Enabled	0	RW	Y
93	6	Discrete1 Mirroring Port Selection	8-bit Uinteger	0 = Port 1 1 = Port 2 2 = Port 3 3 = Port 4 4 = Port 5 5 = Port 6 6 = Port 7 7 = Port 8	0	RW	Y
93	7	Discrete1 Mirroring Channel Selection	8-bit Uinteger	0 = Discrete1, 1 = Discrete2	0	RW	Y
93	8	Discrete1 Mirroring Inversion	8-bit Uinteger	0 = Not Inverted, 1 = Inverted	0	RW	Y
93	9	Discrete2 IO Selection	8-bit Uinteger	1 = PNP Input, 3 = PNP Output with Pull Down	0	RW	Y
93	10	Discrete2 Delay Mode	8-bit Uinteger	0 = Disabled 1 = On Off Delay 2 = On One-shot 3 = Off One-shot 4 = On Pulse-stretcher 5 = Off Pulse-stretcher 6 = Totalizer 7 = Retriggerable On One-shot 8 = Retriggerable Off One-Shot	0	RW	Y
93	11	Discrete2 Delay Timer 1	32-bit Uinteger	0..2147483647 [Discrete2 On Delay, One-shot, Pulse-stretcher time(ms) or Totalizer Count]	0	RW	Y
93	12	Discrete2 Delay Timer 2	32-bit Uinteger	0..2147483647 (Discrete2 Off Delay or Totalizer time)ms	0	RW	Y
93	13	Discrete2 Mirroring Enable	8-bit Uinteger	0 = Disabled, 1 = Enabled	0	RW	Y

Continued on page 17



Continued from page 16

Index	Subindex	Name	Length	Value Range	Default	Access Rights	Data Storage?
93	14	Discrete2 Mirroring Port Selection	8-bit Uinteger	0 = Port 1 1 = Port 2 2 = Port 3 3 = Port 4 4 = Port 5 5 = Port 6 6 = Port 7 7 = Port 8	0	RW	Y
93	15	Discrete2 Mirroring Channel Selection	8-bit Uinteger	0 = Discrete1, 1 = Discrete2	0	RW	Y
93	16	Discrete2 Mirroring Inversion	8-bit Uinteger	0 = Not Inverted, 1 = Inverted	0	RW	Y
<b>94</b>		<b>Port 8 Configuration</b>					
94	1	Discrete1 IO Selection	8-bit Uinteger	1 = PNP Input, 3 = PNP Output with Pull Down	0	RW	Y
94	2	Discrete1 Delay Mode	8-bit Uinteger	0 = Disabled 1 = On Off Delay 2 = On One-shot 3 = Off One-shot 4 = On Pulse-stretcher 5 = Off Pulse-stretcher 6 = Totalizer 7 = Retriggerable On One-shot 8 = Retriggerable Off One-Shot	0	RW	Y
94	3	Discrete1 Delay Timer 1	32-bit Uinteger	0..2147483647 [Discrete1 On Delay, One-shot, Pulse-stretcher time(ms) or Totalizer Count]	0	RW	Y
94	4	Discrete1 Delay Timer 2	32-bit Uinteger	0..2147483647 (Discrete1 Off Delay or Totalizer time)ms	0	RW	Y
94	5	Discrete1 Mirroring Enable	8-bit Uinteger	0 = Disabled, 1 = Enabled	0	RW	Y
94	6	Discrete1 Mirroring Port Selection	8-bit Uinteger	0 = Port 1 1 = Port 2 2 = Port 3 3 = Port 4 4 = Port 5 5 = Port 6 6 = Port 7 7 = Port 8	0	RW	Y
94	7	Discrete1 Mirroring Channel Selection	8-bit Uinteger	0 = Discrete1, 1 = Discrete2	0	RW	Y
94	8	Discrete1 Mirroring Inversion	8-bit Uinteger	0 = Not Inverted, 1 = Inverted	0	RW	Y
94	9	Discrete2 IO Selection	8-bit Uinteger	1 = PNP Input, 3 = PNP Output with Pull Down	0	RW	Y
94	10	Discrete2 Delay Mode	8-bit Uinteger	0 = Disabled 1 = On Off Delay 2 = On One-shot 3 = Off One-shot 4 = On Pulse-stretcher 5 = Off Pulse-stretcher 6 = Totalizer 7 = Retriggerable On One-shot 8 = Retriggerable Off One-Shot	0	RW	Y

Continued on page 18

Continued from page 17

Index	Subindex	Name	Length	Value Range	Default	Access Rights	Data Storage?
94	11	Discrete2 Delay Timer 1	32-bit Uinteger	0..2147483647 [Discrete2 On Delay, One-shot, Pulse-stretcher time(ms) or Totalizer Count]	0	RW	Y
94	12	Discrete2 Delay Timer 2	32-bit Uinteger	0..2147483647 (Discrete2 Off Delay or Totalizer time)ms	0	RW	Y
94	13	Discrete2 Mirroring Enable	8-bit Uinteger	0 = Disabled, 1 = Enabled	0	RW	Y
94	14	Discrete2 Mirroring Port Selection	8-bit Uinteger	0 = Port 1 1 = Port 2 2 = Port 3 3 = Port 4 4 = Port 5 5 = Port 6 6 = Port 7 7 = Port 8	0	RW	Y
94	15	Discrete2 Mirroring Channel Selection	8-bit Uinteger	0 = Discrete1, 1 = Discrete2	0	RW	Y
94	16	Discrete2 Mirroring Inversion	8-bit Uinteger	0 = Not Inverted, 1 = Inverted	0	RW	Y
<b>95</b>		<b>Discrete Host Mirroring Configuration</b>					
95	1	Mirroring Enable	8-bit Uinteger	0 = Disabled, 1 = Enabled	0	RW	Y
95	2	Mirroring Port Selection	8-bit Uinteger	0 = Port 1 1 = Port 2 2 = Port 3 3 = Port 4 4 = Port 5 5 = Port 6 6 = Port 7 7 = Port 8	0	RW	Y
95	3	Mirroring Channel Selection	8-bit Uinteger	0 = Discrete1, 1 = Discrete2	0	RW	Y
95	4	Mirroring Inversion	8-bit Uinteger	0 = Not Inverted, 1 = Inverted	0	RW	Y
95	5	Mirroring Polarity	8-bit Uinteger	0 = NPN Output, 1 = PNP Output	1	RW	Y
95	6	Mirroring Output Type	8-bit Uinteger	0 = Output with Internal Pull Up/Down, 1 = Output Open Collector	0	RW	Y

## IO-Link Events

Events are acyclic transmissions from the IO-Link device to the IO-Link master. Events can be error messages and/or warning or maintenance data.

Code	Type	Name	Description
25376 (0x6320)	Error	Parameter error	Check data sheet and values
36000 (0x8CA0)	Warning	All-time Run Time Event	Event indicating the corresponding configured running time has elapsed.
36001 (0x8CA1)	Warning	Resettable Run Time Event	Event indicating the corresponding configured running time has elapsed.